

EOLS-1303-X Series

Single-Mode 1310nm 100M/155M Duplex SFP Transceiver RoHS6 Compliant

Features

- Operating Data Rate up to 155Mbps
- 15km with 9/125 μm SMF
 40km with 9/125 μm SMF
 80km with 9/125 μm SMF
- Single 3.3V Power Supply and TTL Logic Interface
- Hot-Pluggable SFP Footprint Duplex LC
 Connector Interface
- Class 1 FDA and IEC60825-1 Laser Safety Compliant
- Operating Case Temperature

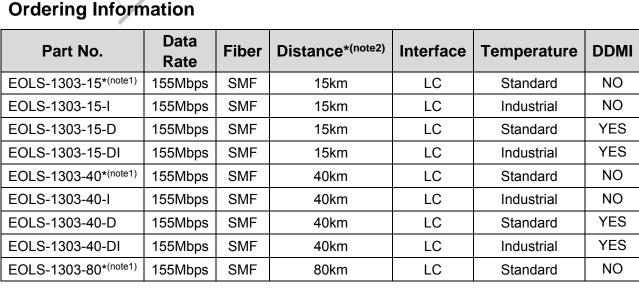
Standard: 0°C~+70°C

Industrial:-40°C~+85°C

- ◆ Compliant with MSA SFP Specification
- ◆ Digital Diagnostic Monitor Interface Compatible

with SFF-8472







Applications

- ◆ Fast Ethernet
- ◆ OC-3 IR-1 / STM-1 (S-1.1)
- ◆ ATM Switches and Routers
- Other Optical Links



EOLS-1303-80-I	155Mbps	SMF	80km	LC	Industrial	NO
EOLS-1303-80-D	155Mbps	SMF	80km	LC	Standard	YES
EOLS-1303-80-DI	155Mbps	SMF	80km	LC	Industrial	YES

Note1: Standard version

Note2: 15km/40km/80km with 9/125 µm SMF

Regulatory Compliance*Note3

Product Certificate	Certificate Number	Applicable Standard
		EN 60950-1:2006+A11+A1+A12+A2
TUV	R50135086	EN 60825-1:2014
		EN 60825-2:2004+A1+A2
111	E247227	UL 60950-1
UL	E317337	CSA C22.2 No. 60950-1-07
		EN 55032:2012
EMC CE	AE 50384190 0001	EN 55032:2015
EIVIC CE	AE 50364190 0001	EN 55024:2010
		EN 55024:2010+A1
'FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA		CDRH 1040.10
ROHS		2011/65/EU

Note3: The above certificate number updated to June 2018, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

Product Description

The EOLS-1303-X series single-mode transceiver is small form factor pluggable module for duplex optical data communications such as Fast Ethernet and OC-3/STM-1 SDH/SONET. It is with the SFP 20-pin connector to allow hot plug capability. This module is designed for single-mode fiber and operates at a nominal wavelength of 1310nm.

The transmitter section uses a multiple quantum well 1310nm laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated GaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

The EOLS-1303-XD series are designed to be compliant with SFF-8472 Multi-source Agreement (MSA).

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	TS	-40	+85	°C
Supply Voltage	VCC	-0.5	3.6	V
Operating Relative Humidity		-	95	%

^{*}Exceeding any one of these values may destroy the device immediately.

^{*}The product image only for reference purpose.

Recommended Operating Conditions

Pa	arameter	Symbol		Min.	Typical	Max.	Unit
Opei	rating Case	То	EOLS-1303-X	0		+70	°C
Ter	mperature	Тс	EOLS-1303-X-I	-40		+85	C
Power S	Supply Voltage		Vcc	3.15	3.3	3.45	V
Power S	Supply Current		Icc			300	mA
Date	OC-3/STM-1				155		Mbps
Rate	FE				100		ivibps

Performance Specifications - Electrical

Parameter		Symbol	Min.	Тур.	Max	Unit	Notes	
Transmitter								
	LVPECL Vin 400 2000 puts(Differential)		2000	mVpp	AC coupled inputs*(Note6)			
Input Imp (Differe		Zin	85	100	115	ohms	Rin > 100 kohms @ DC	
TV Die	Disable		2		Vcc	V		
TX_Dis	Enable		0		0.8	V		
TV EALILT	Fault		2		Vcc+0.3	V		
TX_FAULT	Normal		/ 0		0.5	V		
			Rece	iver				
LVPECL (Differe	•	Vout	400		2000	mVpp	AC coupled outputs*(Note6)	
Output Im (Differe		Zout	85	100	115	ohms		
DV LOS	LOS		2		Vcc+0.3	٧		
RX_LOS	Normal	>	0		0.8	V		
MOD DE	E (0:2)	VoH	2.5			V	With Carial ID	
MOD_DE	r (U.Z)	VoL	0		0.5	V	With Serial ID	

Optical and Electrical Characteristics

(1310nm FP and PIN, 15km)

Parameter	Symbol	Min.	Typical	Max.	Unit			
9µm Core Diameter SMF	L		15		km			
Data Rate			100/155		Mbps			
Transmitter								
Center Wavelength	λc	1260	1310	1360	nm			
Spectral Width (RMS)	Δλ			4	nm			
Average Output Power*(Note4)	Pout	-15		-8	dBm			
Extinction Ratio*(Note5)	ER	8.2			dB			
Rise/Fall Time(20%~80%)	tr/tf			2	ns			



Output Optical Eye*(Note5)	IUT-T G.957 Compliant*(Note8)							
TX_Disable Assert Time	t_off			10	us			
Receiver								
Center Wavelength	λc	1260		1600	nm			
Receiver Sensitivity*(Note7)	Pmin			-28	dBm			
Receiver Overload	Pmax	-8			dBm			
LOS De-Assert	LOSD			-29	dBm			
LOS Assert	LOSA	-42			dBm			
LOS Hysteresis*(Note9)		0.5			dB			

(1310nm FP and PIN, 40km)

(1310IIIII II alia i iii, 40Kiii)					
Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L	\	40		km
Data Rate			100/155	•	Mbps
Tra	nsmitter				
Center Wavelength	λc	1260	1310	1360	nm
Spectral Width (RMS)	Δλ /			3	nm
Average Output Power*(Note4)	Pout	-5		0	dBm
Extinction Ratio*(Note5)	ER	10			dB
Rise/Fall Time(20%~80%)	tr/tf			2	ns
Output Optical Eye*(Note5)	IUT-T G.957 Compliant*(Note8)				
TX_Disable Assert Time	t_off			10	us
Re	eceiver				
Center Wavelength	λc	1260		1600	nm
Receiver Sensitivity*(Note7)	Pmin			-34	dBm
Receiver Overload	Pmax	-10			dBm
LOS De-Assert	LOSD			-35	dBm
LOS Assert	LOSA	-45			dBm
LOS Hysteresis*(Note9)		0.5			dB

(1310nm DFB and PIN, 80km)

Parameter	Symbol	Symbol Min. Typical		Max.	Unit
9µm Core Diameter SMF	L		80		km
Data Rate			100/155		Mbps
Tra	nsmitter				
Centre Wavelength	λc	1260	1310	1360	nm
Spectral Width (-20dB)	Δλ 1		nm		
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power*(Note4)	Pout	0		5	dBm
Extinction Ratio*(Note5)	ER	10			dB
Rise/Fall Time(20%~80%)	tr/tf	tr/tf 2 r		ns	
Output Optical Eye*(Note5)	IUT-T G.957 Compliant*(Note8)				
TX_Disable Assert Time	t_off 10 us			us	
R	eceiver	•	•		



Center Wavelength	λc	1260	1600	nm
Receiver Sensitivity*(Note7)	Pmin		-34	dBm
Receiver Overload	Pmax	-3		dBm
LOS De-Assert	LOSD		-35	dBm
LOS Assert	LOSA	-45		dBm
LOS Hysteresis*(Note9)		0.5		dB

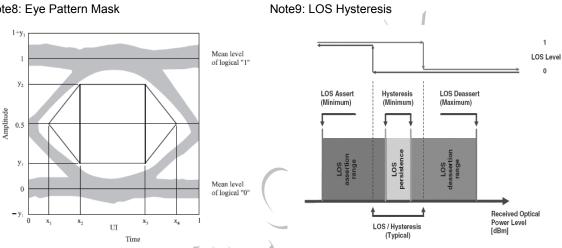
Note4: Output power is power coupled into a 9/125µm single-mode fiber.

Note5: Filtered, measured with a PRBS 223-1 test pattern @155Mbps

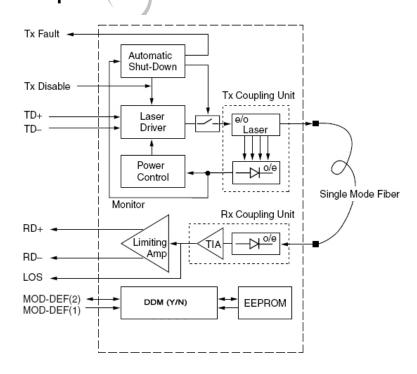
Note6: LVPECL logic, internally AC coupled.

Note7: Minimum average optical power measured at BER less than 1E-12, with a 2²³-1 PRBS and ER=9 dB.

Note8: Eye Pattern Mask

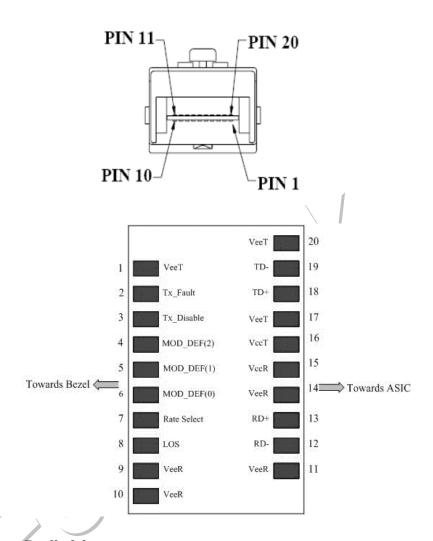


Functional Description of Transceiver





SFP Transceiver Electrical Pad Layout



Pin Function Definitions

Pin Num.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	5)
2	TX Fault	Transmitter Fault Indication	3	1)
3	TX Disable	Transmitter Disable	3	Module disables on high or open
4	MOD-DEF2	Module Definition 2	3	3) 2 wire serial ID interface.
5	MOD-DEF1	Module Definition 1	3	3) 2 wire serial ID interface.
6	MOD-DEF0	Module Definition 0	3	3) Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	4)
9	VeeR	Receiver Ground	1	5)
10	VeeR	Receiver Ground	1	5)



11	VeeR	Receiver Ground	1	5)
12	RD-	Inv. Received Data Out	3	6)
13	RD+	Received Data Out	3	6)
14	VeeR	Receiver Ground	1	5)
15	VccR	Receiver Power	2	7) 3.3 ± 5%
16	VccT	Transmitter Power	2	7) 3.3 ± 5%
17	VeeT	Transmitter Ground	1	5)
18	TD+	Transmit Data In	3	8)
19	TD-	Inv. Transmit Data In	3	8)
20	VeeT	Transmitter Ground	1	5)

Notes

- 1) TX Fault is an open collector/drain output, which should be pulled up with a $4.7K-10K\Omega$ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
- 2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7 10 \text{ K}\Omega$ resistor. Its states are:

Low (0 - 0.8V): Transmitter on

(>0.8, < 2.0V): Undefined

High (2.0 – 3.465V): Transmitter Disabled

Open: Transmitter Disabled

3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10K resistor on the host board. The pull-up voltage shall be VccT or VccR.

Mod-Def 0 is grounded by the module to indicate that the module is present

Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

- 4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a $4.7K-10K\Omega$ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
- 5) VeeR and VeeT may be internally connected within the SFP module.
- 6) RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 400 and 2000mV differential (200 –1000mV single ended) when properly terminated.
- 7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V

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 V4.f

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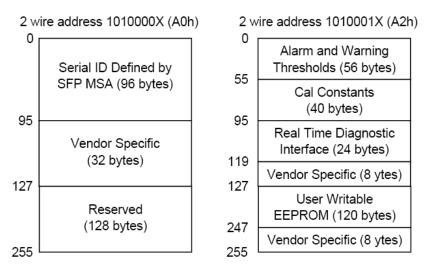
±5% at the SFP connector pin. Maximum supply current is 300Ma. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30Ma greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.

8) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 400 - 2000mV (200 - 1000mV single-ended).

EEPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not write protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 9.3.



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EEPROM Serial ID Memory Contents

Accessing Serial ID Memory uses the 2 wire address 1010000X(A0H). Memory Contents of Serial ID are shown in Table 1.

Table 1 Serial ID Memory Contents

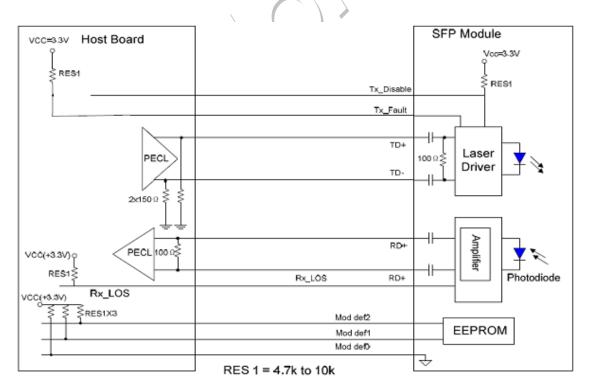
Addr.	Size (Bytes)	Name of Field	Hex	Description					
BASE ID FIELDS									
0	1	Identifier	03	SFP					
1	1	Ext. Identifier	04	SFP function is defined by serial ID only					
2	1	Connector	07	LC Connector					
3-10	8	Transceiver	XX(Note10)	OC 3, Single mode inter. Or long reach					
11	1	Encoding	03	NRZ					
12	1	BR, Nominal	02	155Mbps					
13	1	Reserved	00						
14	1	Length (9µm)km	XX (0F/28/3C)						
15	1	Length(9µm)100m	XX (96/FF/FF)	Transceiver transmit distance					
16	1	Length (50µm) 10m	00	Transceiver transmit distance					
17	1	Length(62.5µm)10m	00						
18	1	Length (Copper)	00	Not compliant					
19	1	Reserved	00						
20-35	16	Vendor name	XX XX XX XX XX XX XX XX ^(Note10) 20 20 20 20 20 20 20 20						
36	1	Reserved	00						
37-39	3	Vendor OUI	XX XX XX ^(Note10)						
40-55	16	Vendor PN	XX						
56-59	4	Vendor rev	XX XX XX XX(Note10)						
60-61	2	Wavelength	05 1E	1310nm					
62	1	Reserved	00						
63	1	CC_BASE	Check Sum (Variable)	Check code for Base ID Fields					
		E	XTENDED ID FIELD	s					
64-65	2	Options	00 1A	TX_DISABLE, TX_FAULT and Loss of Signal implemented.					
66	1	BR,max	00						
67	1	BR,min	00						



68-83	16	Vendor SN	XX XX XX XXXX XX	Serial Number of transceiver (ASCII)				
			XX XX 20 20 20 20	For example "B000822".				
			20 20 20 20 ^(Note10)	Tot example B000022.				
84-91	8	Date code	XX XX XX XX XX XX	Manufactory date code.				
04-91	0	Date Code	XX XX ^(Note10)	For example "080405".				
92	1	Diagnostic	XX(Note10)	Digital diagnostic monitoring				
92	Į.	Monitoring Type	AA (*** */	implemented				
93	1	Enhanced Options	XX ^(Note10)	Optional flags				
04	1	SFF_8472	XX(Note10)	01 for diagnostics (Rev9.3				
94	ı	Compliance	XX (1860.6)	SFF-8472).				
95	1	CC EXT	Check Sum	Check sum for Extended ID Field.				
90		CC_EXT	(Variable)	Check sulli for Exterided ID Field.				
VENDOR SPECIFIC ID FIELDS								
96-127	32	Vendor Specific	Read only	Depends on Customer Information				
128-255	128	Reserved	Read only					

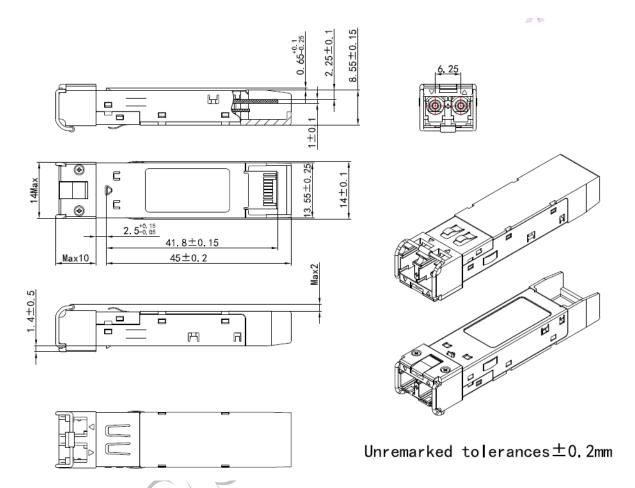
Note10: The "xx" byte should be filled in according to practical case. For more information, please refer to the related document of SFP Multi-Source Agreement (MSA).

Recommend Circuit Schematic





Mechanical Specifications

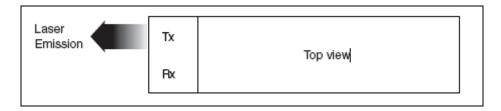


^{*}This 2D drawing only for reference, please check with Eoptolink before ordering.

Laser Emission Data

Wavelength	1310nm
Total output power (as defined by FDA: 7mm aperture at 20cm distance)	<0.195Mw
Total output power (as defined by IEC: 7mm aperture at 10cm distance)	<15.6Mw
Beam divergence	12.5°

Laser Emission







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Revision History

Revision	Initiated	Reviewed	Approved	Revision History	Release Date
V3.a	Kelly			Update LOGO&PN.	June 16, 2011
V3.b	Jans			Update the Sensitivity of EOLS-1303-15.	September 8, 2011
V4.a	Jans	Kelly		Update photo, correct some spelling mistakes.	Sep 22, 2011
V4.b	Jans			Modify the parameter of 80km.	July 18,2012
V4.c	Angela	Kelly		Update pin definition Note3	Feb 4,2013
V4.d	Angela	Kelly/Vina/Dean	Phlio	Update the regulatory compliance, LOSA of 15km and 2D drawing.	May 03,2016
V4.e	Angela	Kelly/William/ Chao.Wang		Updated the product image and 2D drawing.	April 17, 2018
V4.f	Angela	Kelly/William/ Chao.Wang		Updated the regulatory compliance and 2D drawing.	September 14, 2018

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