

EOLX-1596-80-X

1550nm XFP Single-Mode for 10GbE/FC/SDH/SONET Duplex XFP Transceiver RoHS6 Compliant

Features

- Supports 9.95Gb/s to 11.3Gb/s Bit Rates
- Hot-pluggable XFP Footprint
- ◆ Maximum Link Length up to 80km
- ◆ Temperature-Stabilized EML transmitter
- ◆ Duplex LC Connector
- ◆ Built-in Digital Diagnostic Functions
- ◆ Case Operating Temperature:

Standard: 0°C to 70°C

Industrial:-40°C~85°C

◆ No external clock required



Applications

- ◆ OC192/ STM 64
- ◆ 10GBASE-ZR/ZW 10G Ethernet
- ◆ Fiber Channel
- ◆ P1L1-2D2
- ♦ ITU-T G.709

Ordering Information

| Part No. | Data Rate | Laser | Fiber Type | Distance | Optical Interface | Temp. |
|----------------|---------------|-------|---------------|----------|-------------------|------------|
| EOLX-1596-80 | Up to11.3Gbps | EML | SMF | 80km | LC | Standard |
| EOLX-1596-80-I | Up to11.3Gbps | EML | SMF | 80km | LC | Industrial |

^{*}The product image only for reference purpose.



Regulatory Compliance Note1

| Product Certificate | Certificate Number | Applicable Standard |
|----------------------------|--------------------|-------------------------------|
| | | EN 60950-1:2006+A11+A1+A12+A2 |
| TUV | R50135086 | EN 60825-1:2014 |
| | | EN 60825-2:2004+A1+A2 |
| UL | F247227 | UL 60950-1 |
| UL | E317337 | CSA C22.2 No. 60950-1-07 |
| EMC CE | AE 50285865 0001 | EN 55022:2010 |
| EIVIC CE | AE 30203003 000 I | EN 55024:2010 |
| FCC | WTF14F0514417E | 47 CFR PART 15 OCT., 2013 |
| FDA | 1 | CDRH 1040.10 |
| ROHS | 1 | 2011/65/EU |

Note1: The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

Product Description

The EOLX-1596-80 series single mode transceiver is small form factor pluggable module for duplex optical data communications such as 10GBASE-ZR/ZW defined by IEEE 802.3ae. It is with the XFP 30-pin connector to allow hot plug capability.

This module is designed for single mode fiber and operates at a nominal wavelength of 1550 nm. The transmitter section uses a 1550nm EML, which is class 1 laser compliant according to International Safety Standard IEC-60825.

The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

Absolute Maximum Ratings*Note2

| Parameter | Symbol | Min | Тур | Max | Unit |
|--------------------------|--------|------|-----|-----|------|
| Maximum Supply Voltage 1 | Vcc3 | -0.5 | - | 4.0 | V |
| Maximum Supply Voltage 2 | Vcc5 | -0.5 | - | 6.0 | V |
| Storage Temperature | Ts | -40 | - | 85 | °C |

Note2: Exceeding any one of these values may destroy the device permanently.

Recommended Operating Condition

| Parameter | Symbol | | Min | Тур | Max | Units |
|------------------|--------|----------------|------|-----|------|-------|
| Supply Voltage 1 | Vcc3 | | 3.13 | 3.3 | 3.45 | ٧ |
| Supply Voltage 2 | Vcc5 | | 4.75 | 5 | 5.25 | V |
| Case Operating | To | EOLX-1596-80 | 0 | - | 70 | °C |
| Temperature | Тс | EOLX-1596-80-I | -40 | - | 85 | °C |

Electrical Characteristics



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| Parameter | Symbol | Min | Тур | Max | Unit |
|--------------------------------------|------------------------|-----------|-----|----------|------|
| Main Supply Voltage | Vcc5 | 4.75 | - | 5.25 | V |
| Supply Voltage #2 | Vcc3 | 3.13 | - | 3.45 | V |
| Supply Current – Vcc5 supply | Icc5 | - | - | 370 | mA |
| Supply Current – Vcc3 supply | lcc3 | - | - | 500 | mA |
| Module Total Power | Р | - | - | 3.5 | W |
| | Transmitter | | | | |
| Input Differential Impedance*Note3 | Rin | - | 100 | - | Ω |
| Differential Data Input Swing | Vin,pp | 120 | - | 820 | mV |
| Transmit Disable Voltage | V_D | 2.0 | - | Vcc | V |
| Transmit Enable Voltage | V _{EN} | GND | - | GND+ 0.8 | V |
| Transmit Disable Assert Time | | - | - | 10 | us |
| | Receiver | | | | |
| Differential Data Output Swing*Note3 | Vout,pp | 340 | 650 | 850 | mV |
| Rise Time (20– 80%) | tr | - | - | 38 | ps |
| Fall Time (20– 80%) | tf | - | - | 38 | ps |
| LOS Fault*Note4 | V _{LOS fault} | Vcc - 0.5 | - | VccHOST | V |
| LOS Normal*Note4 | V _{LOS norm} | GND | - | GND+0.5 | V |

Note3: After internal AC coupling

Note4.:Loss of signal is open collector. Logic 0 indicates normal operation; logic 1 indicates no signal detected.

Optical Characteristics

| Parameter | Symbol | Min | Тур | Max | Unit |
|---------------------------------------|----------------------|----------------------------|----------|------|-------|
| | Transmit | ter | <u> </u> | | |
| Output Power @ 9/125 SMF | Pout | 0 | - | +4 | dBm |
| Optical Wavelength | λς | 1530 | - | 1565 | nm |
| Spectral Width (-20dB) | Δλ | - | - | 1 | nm |
| Optical Extinction Ratio@10.3Gb/s | ER | 9 | - | - | dB |
| Average Launch Power of OFF | D | | | -30 | dBm |
| Transmitter | P _{OFF} | - | - | -30 | |
| TX Jitter Generation (Peak-to-Peak) | T_{Xj} | ı | - | 0.1 | UI |
| TX Jitter Generation (RMS) | T_{XjRMS} | ı | - | 0.01 | UI |
| Relative Intensity Noise | RIN | - | - | -130 | dB/Hz |
| Eye Mask | | Compliant with ITU-T G.691 | | | |
| | Receive | r | | | |
| Receiver Sensitivity@ 9.95Gb/s*Note5 | Pmin | - | - | -24 | dBm |
| Receiver Sensitivity @ 10.3Gb/s*Note5 | Pmin | - | - | -24 | dBm |
| Overload Power | Pmax | -7 | - | - | dBm |
| Optical Center Wavelength | λς | 1270 | 1550 | 1600 | nm |
| Receiver Reflectance | Rf | - | - | -27 | dB |
| LOS De-Assert | LOS _{DEASS} | - | - | -26 | dBm |



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| LOS Assert | LOS _{ASS} | -38 | - | - | dBm |
|----------------|--------------------|-----|---|---|-----|
| LOS Hysteresis | | 0.5 | - | - | dB |

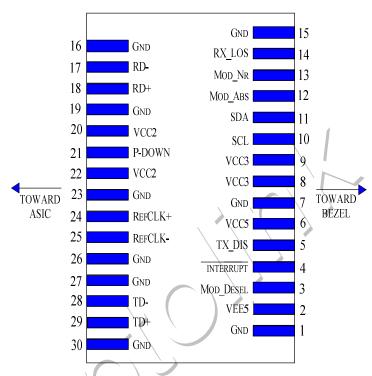
Note5: Back to back, measured with a PRBS 2³¹-1 test pattern and ER=9dB, BER 1X10⁻¹².

Pin Descriptions

| Pin | Symbol | Name/Description |
|-----|------------|---|
| 1 | GND | Module Ground |
| 2 | VEE5 | Optional –5.2 Power Supply – Not required |
| 3 | Mod-Desel | Module De-select; When held low allows the module to respond to |
| | | 2-wire serial interface commands |
| 4 | Interrupt | Interrupt (bar); Indicates presence of an important condition which can |
| | | be read over the serial 2-wire interface |
| 5 | TX_DIS | Transmitter Disable; Transmitter laser source turned off |
| 6 | VCC5 | +5 Power Supply |
| 7 | GND | Module Ground |
| 8 | VCC3 | +3.3V Power Supply |
| 9 | VCC3 | +3.3V Power Supply |
| 10 | SCL | Serial 2-wire interface clock line |
| 11 | SDA | Serial 2-wire interface data line |
| 12 | Mod_Abs | Module Absent; Indicates module is not present. Grounded in the |
| | | module. |
| 13 | Mod_NR | Module Not Ready; |
| 14 | RX_LOS | Receiver Loss of Signal indicator |
| 15 | GND | Module Ground |
| 16 | GND | Module Ground |
| 17 | RD- | Receiver inverted data output |
| 18 | RD+ | Receiver non-inverted data output |
| 19 | GND | Module Ground |
| 20 | VCC2 | +1.8V Power Supply – Not required |
| | | Power Down; When high, places the module in the low power |
| | | stand-by mode and on the falling edge of P_Down initiates a module |
| 21 | P_Down/RST | reset |
| | | Reset; The falling edge initiates a complete reset of the module |
| | | including the 2-wire serial interface, equivalent to a power cycle. |
| 22 | VCC2 | +1.8V Power Supply – Not required |
| 23 | GND | Module Ground |
| 24 | RefCLK+ | Reference Clock non-inverted input, AC coupled on the host board – |
| | | Not required |
| 25 | RefCLK- | Reference Clock inverted input, AC coupled on the host board – Not |
| | | required |
| 26 | GND | Module Ground |
| 27 | GND | Module Ground |

| 28 | TD- | Transmitter inverted data input |
|----|-----|-------------------------------------|
| 29 | TD+ | Transmitter non-inverted data input |
| 30 | GND | Module Ground |

Pin arrangement



Pin Numbers and Name

Digital Diagnostic Functions

Eoptolink's EOLX-1596-80 Small Form Factor 10Gbps (XFP) transceivers are compliant with the current XFP Multi-Source Agreement (MSA) Specification Rev 4.5.

As defined by the XFP MSA, Eoptolink XFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- ◆ Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- ◆ Transceiver supply voltage

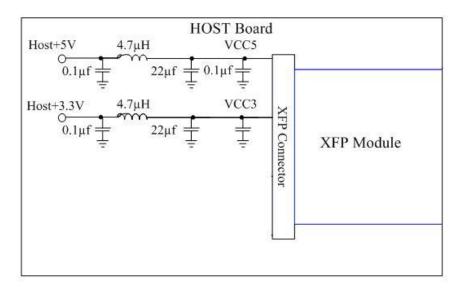
It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller inside the transceiver, which is accessed through the 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL pin) is generated by the host. The positive edge clocks data into the XFP transceiver into those segments of its memory map that are not write-protected. The negative edge clocks data from the XFP

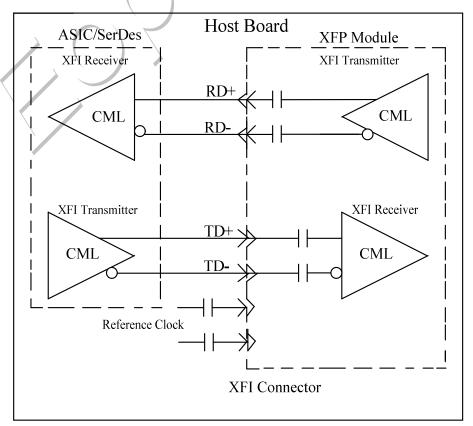


transceiver. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially. The 2-wire serial interface provides sequential or random access to the 8 bit parameters, addressed from 000h to the maximum address of the memory.

Recommended Host Board Power Supply Circuit



Recommended High-Speed Interface Circuit

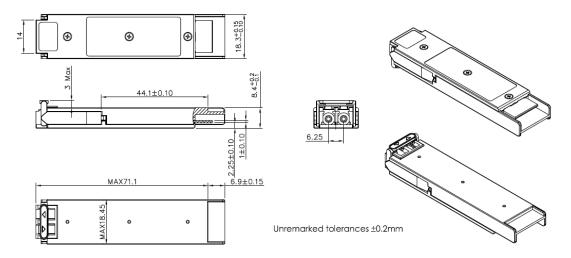


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Mechanical Specifications

Eoptolink's XFP transceivers are compliant with the dimensions defined by the XFP Multi-Sourcing Agreement (MSA).



*This 2D drawing only for reference, please check with Eoptolink before ordering.

Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

Obtaining Document

You can visit our website: http://www.eoptolink.com

Or contact Eoptolink Technology Inc., Ltd. listed at the end of the documentation to get the latest documents.

Revision History

| Revision | Initiated | Reviewed | Approved | Revision History | Release Date |
|----------|-----------------|--------------|----------|---------------------------------|-----------------|
| V3.a | Kelly Cao | | | Update application. | June 7, 2011 |
| V3.b | Alex/Towni e | Kelly | | Updating photo. | Aug 10, 2011 |
| V3.c | Kelly | | | Updating sen.@10.7G. | Sep 15, 2011 |
| V3.d | Abby | Kelly | | Update LOS De-assert/Assert. | Sep 6,2012 |
| V3.e | JP,jiang, | Kelly, Fing. | | Update max date | Jan 10, 2013 |

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| _ | Angela | | rates and Icc | |
|------|-----------------|--|--|---------------|
| V3.f | Fing, Angela | Kelly | Update temperature range | Feb 16,2013 |
| V3.g | Angela | Kelly | Delete 1200-SM-LL-L application. | July 19, 2013 |
| V3.h | Elaine | Kelly/Angela Fing/JP/Eason/ JasonVina/ Dean/ Yiwei.Chen/ Chao.Wang | Update the extended temperature into industrial temperature, regulatory compliance and 2D drawing. | Jan 11,2017 |

Notice:

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