

EOLX-1696-14X

CWDM XFP Single-Mode for 10GbE/10GFC/SDH/SONET Duplex XFP Transceiver RoHS6 Compliant

Features

- ◆ Supports 9.95Gb/s to 11.1Gb/s Bit Rates
- Hot-Pluggable XFP Footprint
- ◆ Compliant with XFP MSA
- 8-Wavelengths CWDM EML Transmitter
 from 1470nm to 1610nm, with Step 20nm
- 14dB power budget at least
- Duplex LC Connector
- ◆ Power Dissipation < 3.5W
- ◆ Case Operation Temperature

 Standard: 0°C to 70°C

Industrial:-40°C to 85°C

2-Wire Interface for Integrated Digital
 Diagnostic Monitoring



Applications

- ◆ SONET/SDH
- ◆ 10GBASE-ER/EW 10G Ethernet
- ◆ 1200-SM-LL-L 10G Fiber Channel
- ◆ 10GE over G.709 at 11.09Gbps
- ◆ OC192 over FEC at 10.709Gbps

Ordering Information

Part No.	Data Rate	Laser	Fiber Distance		Interface	Temp
EOLX-1696-14X*(note1)	10G	CWDM EML	SMF	14dB power budget	LC	Standard
EOLX-1696-14XI	10G	CWDM EML	SMF	14dB power budget	LC	Industrial

Note1: X refers to CWDM Wavelength, from 1470nm to 1610nm, X=K, L, · · · and R;

^{*}The product image only for reference purpose.



CWDM* Wavelength (0~70°C)

Band	Nomenclature	Wavelength(nm)				
Baria	Nomenciature	Min. Typ. 1464 1470 1484 1490 1504 1510 1524 1530 1544 1550 1564 1570	Тур.	Max.		
	К	1464	1470	1477.5		
S-band Short	L	1484	1490	1497.5		
Wavelength	M	1504	1510	1517.5		
	N	1524	1530	1537.5		
C-band Conventional	0	1544	1550	1557.5		
	Р	1564	1570	1577.5		
L-band Long Wavelength	Q	1584	1590	1597.5		
311 9 11 21 21 21 21 3	R	1604	1610	1617.5		

CWDM*: 8 Wavelengths from 1470nm to 1610nm, each step 20nm.

Regulatory Compliance*

Product Certificate	Certificate Number	Applicable Standard
		EN 60950-1:2006+A11+A1+A12+A2
TUV	R50135086	EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
UL	E317337	CSA C22.2 No. 60950-1-07
		EN 55032:2012
EMC CE	AE 50384190 0001	EN 55032:2015
EIVIC CE	AE 50364190 0001	EN 55024:2010
		EN 55024:2010+A1
'FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	/	CDRH 1040.10
ROHS	/	2011/65/EU

^{*:} The above certificate number updated to June 2018, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

Product Description

The EOLX-1696-14X series optical transceiver is designed for fiber communications application such as 10G Ethernet (10GBASE-ER/EW) and 10G Fiber Channel (1200-SM-LL-L), which fully compliant with the specification of XFP MSA Rev 4.5.

This module is designed for single mode fiber and operates at a nominal wavelength of CWDM wavelength. There are eight center wavelengths available from 1470nm to 1610nm, with each step 20nm. A guaranteed optical link budget of 14dB is offered.





supply is needed. The optical output can be disabled by LVTTL logic high-level input of TX_DIS. Loss of signal (RX_LOS) output is provided to indicate the loss of an input optical signal of receiver. This module provides digital diagnostic functions via a 2-wire serial interface as defined by the XFP MSA Rev 4.5.

Absolute Maximum Ratings

Parameter	Symbol		Min	Max	Unit		
Maximum Supply Voltage	Vcc		Vcc		-0.5	4.0	V
Storage Temperature	T _S		-40	85	°C		
Case Operating	т	EOLX-1696-14X	0	70	°C		
Temperature	T _C	EOLX-1696-14XI	-40	85	°C		

Recommend Operating Condition

Parameter	Symbol		Min	Typical	Max	Units
Operating Temperature	т.	EOLX-1696-14X	0		70	°C
	Tc	EOLX-1696-14XI	-40		85	°C
Supply Voltage 1		Vcc3		3.3	3.45	V
Supply Voltage 2		Vcc5	4.75	5	5.25	V
Supply Current-Vcc3 supply		lcc3			300	mA
Supply Current-Vcc5 supply		lcc5			750	mA
Module Total Power		Р			3.5	W

Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Note		
Transmitter								
Input Differential Impedance	Rin		100		Ω	1		
Differential Data Input Swing	Vin, pp	180		820	mV			
Transmit Disable Voltage	V_{DIS}	2.0		Vcc	V			
Transmit Enable Voltage	V_{EN}	GND		GND+ 0.8	V			
Transmit Disable Assert Time				10	us			
		Receiver						
Differential Data Output Swing	Vout, pp	340	650	850	mV			
Data Output Rise Time	tr			38	ps	2		
Data Output Fall Time	tf			38	ps	2		
LOS Fault	V _{LOS fault}	V _{cc-0.5}		V _{cc HOST}	V	3		
LOS Normal	V _{LOS norm}	GND		GND+0.5	V	3		
Power Supply Rejection	PSR	See Note 4 below			4			

Notes:

- 1. After internal AC coupling.
- 2.20 80%.
- 3. Loss of Signal is open collector to be pulled up with a 4.7k 10kohm resistor to 3.15 3.6V.

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Logic 0 indicates normal operation; logic 1 indicates no signal detected.

4. Reference the Section 2.7 of the XFP MSA Rev 4.5.

Optical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Note		
Transmitter								
Optical Modulation Amplitude	P _{OMA}	-1		+4.4	dBm	1		
Output Opt. Pwr: 9/125 SMF	Pout	-0.9		+4.0	dBm			
Optical Extinction Ratio	ER	8.2			dB			
Optical Wavelength	λ	λс–6	λς	λc+7.5	nm	2		
-20dB Spectrum Width	Δλ			1	nm			
Side Mode Suppression Ratio	SMSR	30			dB			
Path Penalty	Рр			2.5	dB			
Average Launch Power of OFF Transmitter	P _{OFF}			-30	dBm			
TX Jitter	TXj	Per 802.3ae requirements						
Relative Intensity Noise	RIN			-128	dB/Hz			
	I	Receiver						
Receiver Sensitivity @ 10.3125Gb/s	Pmin			-15	dBm	3		
Maximum Input Power	Pmax	+0.5			dBm			
Optical Center Wavelength	λ	1260		1620	nm			
Receiver Reflectance	Rrf			-12	dB			
LOS De-Assert	LOS _D			-17	dBm			
LOS Assert	LOSA	-29			dBm			
LOS Hysteresis		1			dB			

Notes

- 1. Output is coupled into a 9/125µm SMF.
- 2. ITU-T G.694.2 CWDM wavelength from 1470nm to 1610nm, each step 20nm.
- 3. Average received power; BER less than 1E-12 and PRBS 2³¹-1 test pattern.

Pin Descriptions

Pin	Logic	Symbol	Name/Description	Notes
1		GND	Module Ground	1
2		VEE5	Optional –5.2 Power Supply – Not Required	
			Module De-select; When held low allows the	
3	LVTTL-I	Mod-Desel	module to respond to 2-wire serial interface	
			commands	
			/Interrupt; Indicates presence of an important	
4	LVTTL-O	/Interrupt	condition which can be read over the serial 2-wire	2
			interface	



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5	LVTTL-I	TX_DIS	Transmitter Disable; Transmitter laser source turned off	
6		VCC5	+5 Power Supply	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I	SCL	Serial 2-wire interface clock line	2
11	LVTTL- I/O	SDA	Serial 2-wire interface data line	2
12	LVTTL-O	Mod_Abs	Module Absent; Indicates module is not present. Grounded in the module.	2
13	LVTTL-O	Mod_NR	Module Not Ready;	2
14	LVTTL-O	RX_LOS	Receiver Loss of Signal indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver inverted data output	
18	CML-O	RD+	Receiver non-inverted data output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply – Not required	
04	1.\/TT1_1	P_Down/R	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module reset	
21	LVTTL-I	ST	Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply – Not required	
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board – Not required	3
25	PECL-I	RefCLK-	Reference Clock inverted input, AC coupled on the host board – Not required	3
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter inverted data input	
29	CML-I	TD+	Transmitter non-inverted data input	
30		GND	Module Ground	1

Notes:

- 1. Module circuit ground is isolated from module chassis ground within the module.
- 2. Open connect should be pulled up with 4.7k 10k ohm on host board to a voltage between 3.15V and 3.6V.
- 3. A Reference Clock input is not required.



Host Board Connector Pin Out

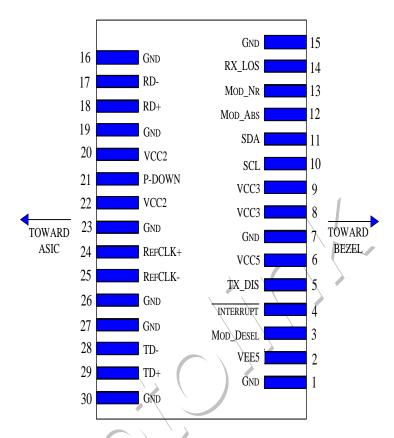


Diagram of Host Board Connector Block Pin Numbers and Name General Specifications

Parameter	Symbol	Min	Typical	Max	Units	Note
Bit Rate	BR	9.95		11.1	Gb/s	
Bit Error Ratio	BER			10 ⁻¹²		1

Notes:

1. Tested 9.95G with 231 – 1 PRBS pattern.

Digital Diagnostic Functions

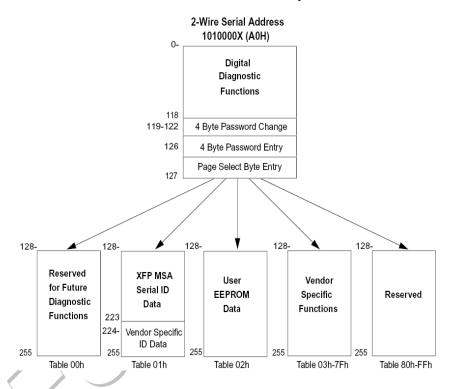
Eoptolink's Small Form Factor 10Gbps (XFP) transceiver is compliant with the current XFP Multi-Source Agreement (MSA) Specification Rev 4.5.

As defined by the XFP MSA, Eoptolink XFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

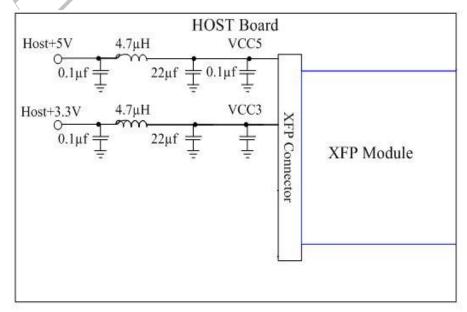
- ◆ Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- Aux Monitoring



It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range. The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller inside the transceiver, which is accessed through the 2-wire serial interface. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially. The 2-wire serial interface provides sequential or random access to the 8 bit parameters, addressed from 00h to the maximum address of the memory.

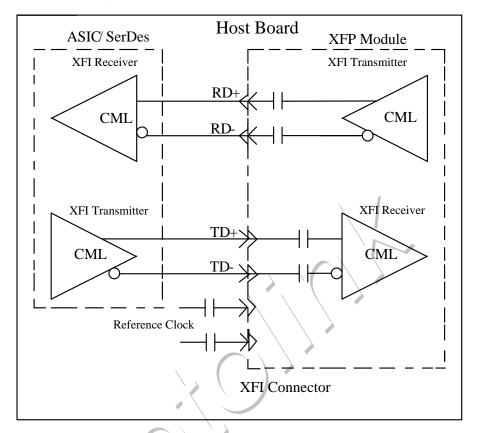


Recommended Host Board Power Supply Circuit



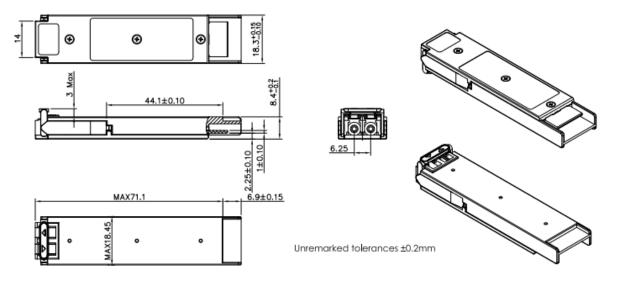


Recommended High-Speed Interface Circuit



Mechanical Specifications

Eoptolink's XFP transceivers are compliant with the dimensions defined by the XFP Multi-Sourcing Agreement (MSA).



*This 2D drawing only for reference, please check with Eoptolink before ordering.



Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

Obtaining Document

You can visit our website:http://www.eoptolink.com

Or contact Eoptolink Technology Inc., Ltd. Listed at the end of the documentation to get the latest documents.

Revision History

ъ				Revision	Release
Revision	Initiated	Reviewed	Approved	History	Date
V1.a	Kelly.Cao	Florence.Dai		Released.	Jan 12, 2009
V1.b	Kelly.Cao			Adding the complete CWDM wavelength.	June 24, 2009
V1.c	Kelly.Cao			Revise the component and CWDM wavelength information.	Oct 10, 2009
V1.d	Kelly.Cao	<i></i>		Add the application description.	Nov 6, 2009
V1.e	Kelly.Cao			Correct sensitivity description.	Dec 30, 2009
V1.f	Kelly.Cao			Updating the mechanical graph.	Apr 15, 2010
V1.g	Kelly.Cao			Update LOGO&PN, add EEPROM map.	July 22, 2011
V2.0	Alex/Townie	Kelly.Cao		Update photo and power supply schematic	Aug 10, 2011
V2.a	Kelly			Update Er &	Sep 19,
				supply circuit. Correct Rx max.	2011 Nov 8,
V2.b	Kelly			Wavelength.	2011



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V2.c	Angela	Abby	Update LOS Deassert/Assert.	Sep 17, 2012
V2.d	Abby	Kelly	Update Path Penalty from 2dB to 2.5dB	Dec 18, 2013
V2.e	Elaine	Torres/Fing/ Eason/Chao.Wang/ Kelly/Angela	Add the industrial temperature, update the regulatory compliance and the mechanical specifications.	Aug 14, 2018

Notice:

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